

FIG. 1
(Prior Art)

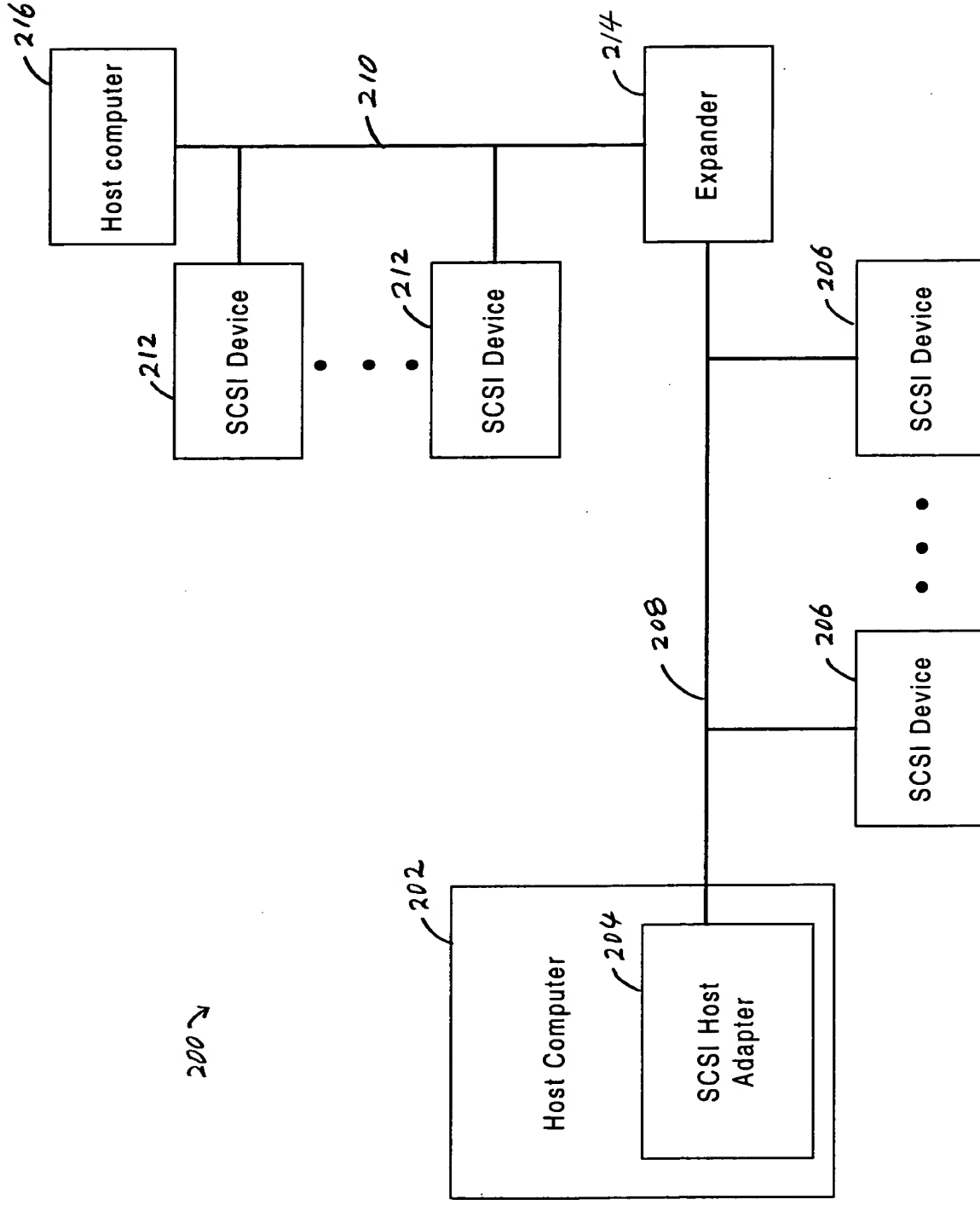


FIG. 2
(Prior Art)

300

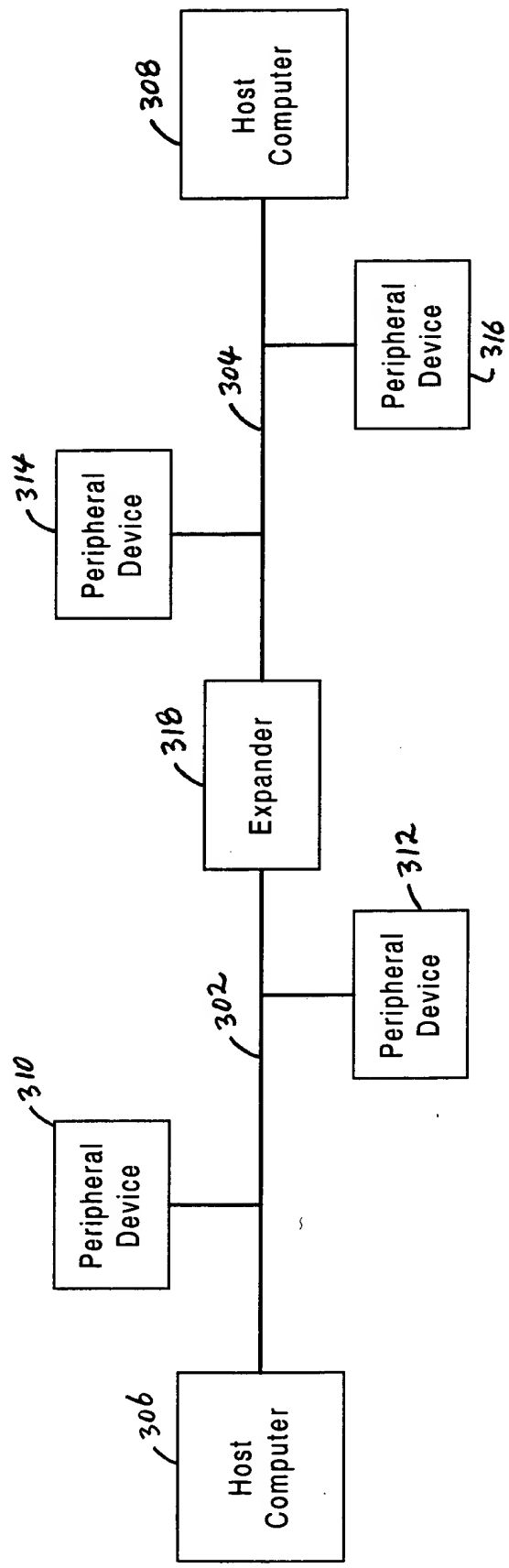


FIG. 3

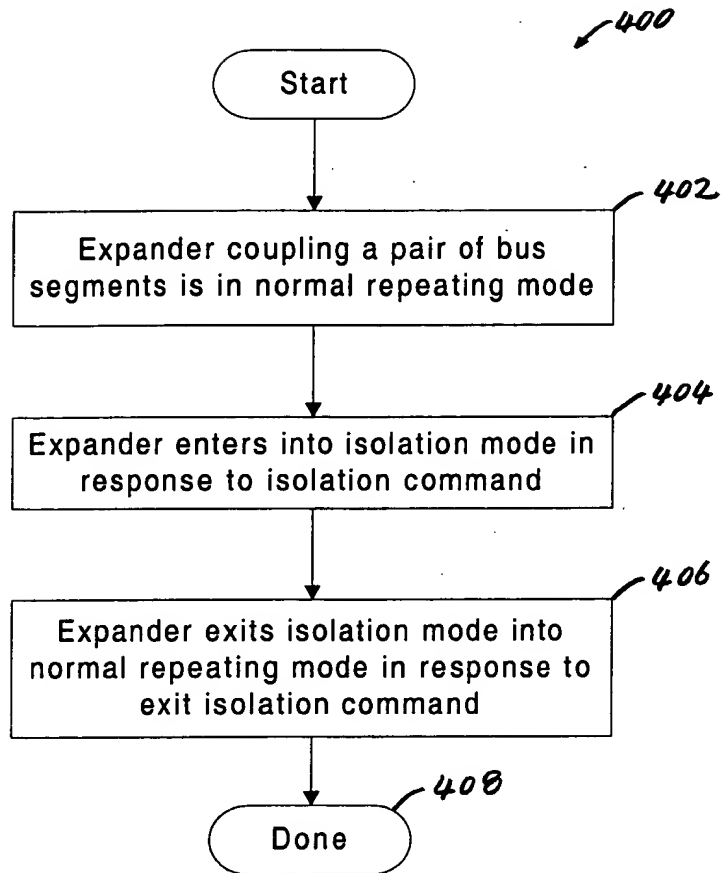


FIG. 4

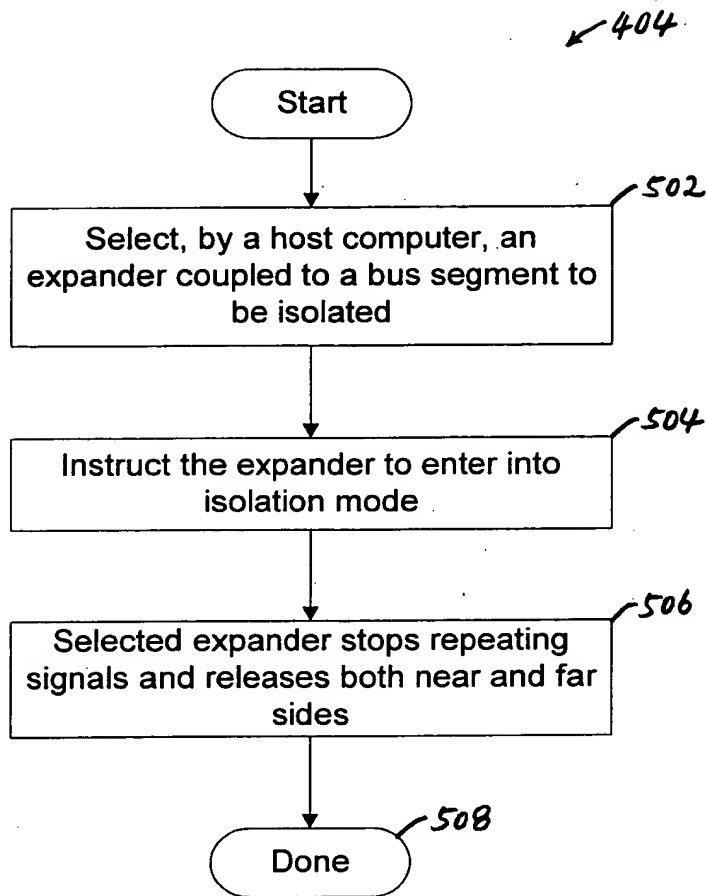


FIG. 5A

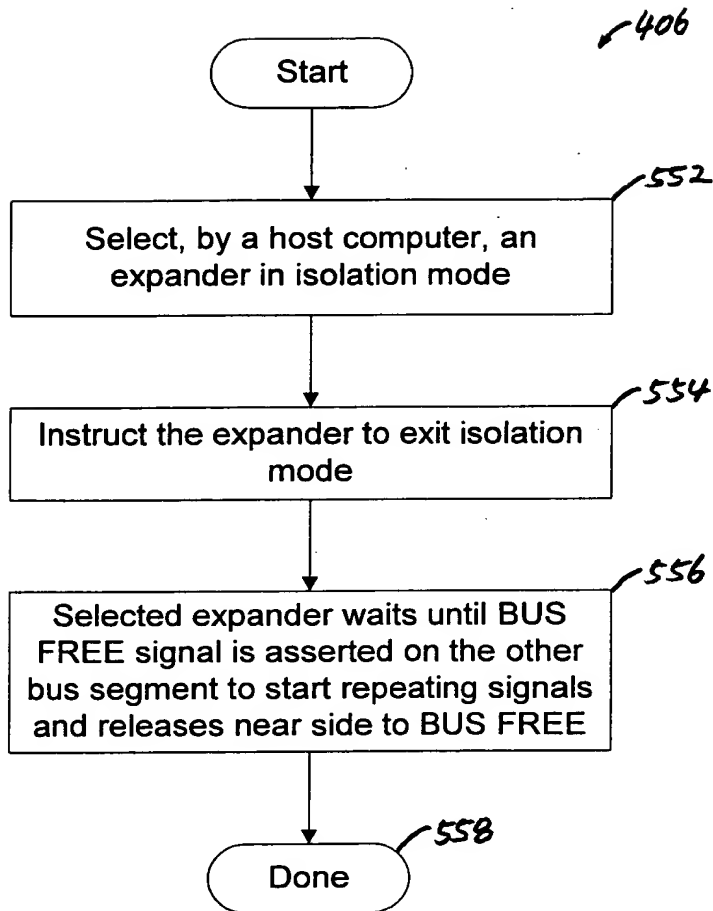


FIG. 5B

FIG. 6 is a block diagram of a SCSI controller 602, in accordance with the present invention. The SCSI controller 602 includes a segment controller 608 and an I/O interface 604. The I/O interface 604 is connected to a data and control signal bus 302. The data and control signal bus 302 carries signals such as REQ_ISOLATE and REQ_EXIT_ISOLATE. The I/O interface 604 includes a first buffer 612 and a second buffer 614. The first buffer 612 is connected to the data and control signal bus 302 and the segment controller 608. The second buffer 614 is connected to the segment controller 608 and the data and control signal bus 302. The segment controller 608 is also connected to an ISOLATE signal 610. The ISOLATE signal 610 is connected to the I/O interface 604 and the I/O interface 606. The I/O interface 606 is connected to a data and control signal bus 304. The data and control signal bus 304 carries signals such as REQ_ISOLATE and REQ_EXIT_ISOLATE. The I/O interface 606 includes a first buffer 616 and a second buffer 618. The first buffer 616 is connected to the data and control signal bus 304 and the segment controller 608. The second buffer 618 is connected to the segment controller 608 and the data and control signal bus 304.

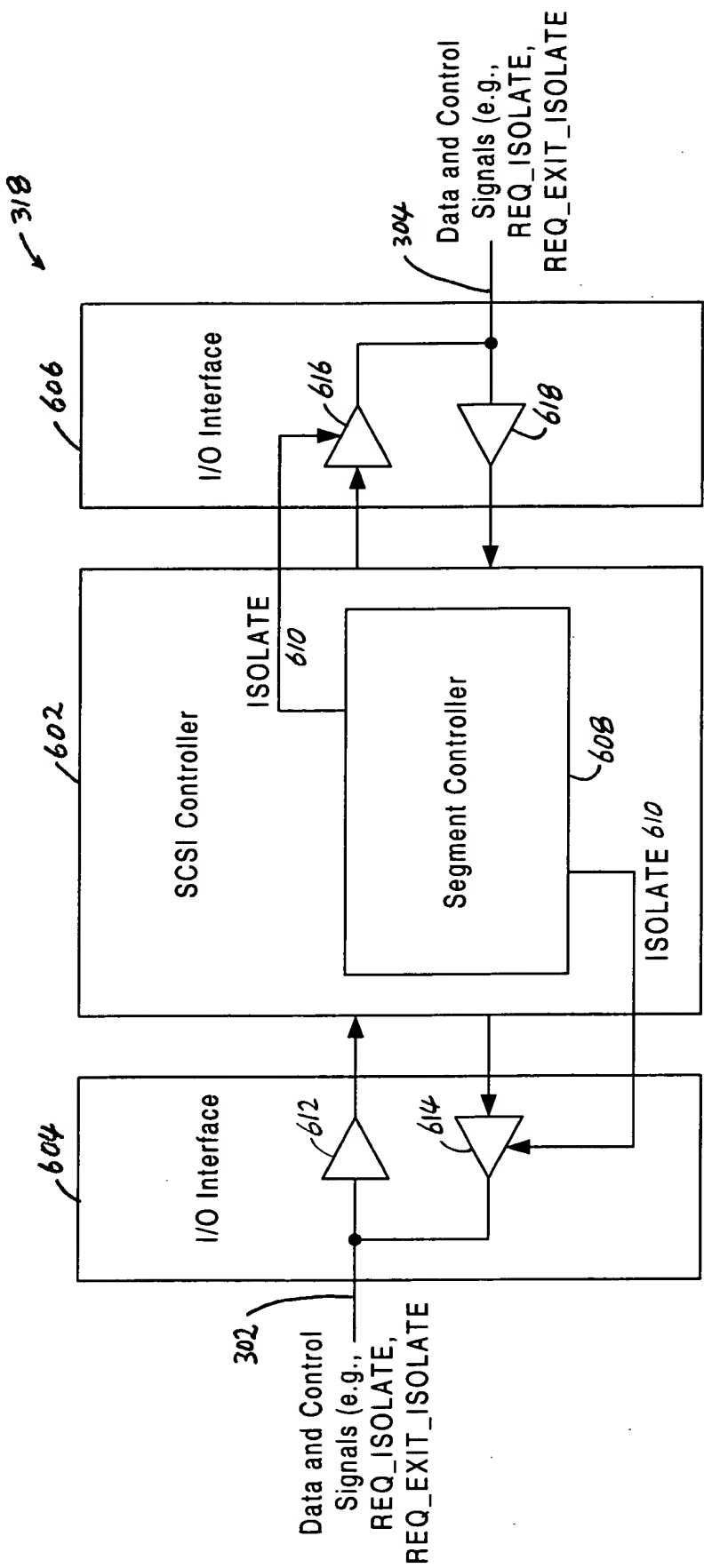


FIG. 6